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MAY 1 2 2005

By Sinda Co Univa Date May 12, 2005

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Inventor(s): Robert M.R. Neff

Group Art Unit: 2816

Serial No.: 10/061,504

Examiner: Linh M. Nguyen

Filed: 1 February 2002

Title: Interleaved Clock Signal Generator Having Delay Chain and Divider Architecture

Atty Docket: 10020205-1

AMENDMENT UNDER 37 CFR § 1.116

Commissioner for Patents P.O. Box 1450 Alexandria VA 22313-1450 Place onto

Six:

In response to the Official Action dated 15 March 2005, the applicant respectfully requests entry of the following amendments: